

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, A. Rangampet – 517 102
Department of Electrical & Electronics Engineering

FUNDED PROJECTS

Sl. No.	Name of the Faculty	Title of the Project	Funding Agency	Amount (Rs.)	Status
1	Mr. K. Leleedhar Rao	Boost-Up Alternate Power Generation for Plead Irrigation	AQIS-RPS	22,72,000	Under Communication
2	Dr. T. Devaraju & Dr. V. Arun	Development of compact asymmetrical multilevel inverter with different pulse width modulation techniques	CPRI	24,25,000	Under Communication
3	Dr. T. Devaraju	Sustainable livelihood development of yandadi tribe through tangible technology and training	DST-SEED	1,99,00,000	Under Communication
4	Dr. T. Devaraju & Dr G Hari Krishnan	Hands on training for school students in IoT, sensors and Robotics.	DST-ICPS	22,88,000	Under Communication
5	Dr.V.Arun &	Power quality enhancement in Rural Residential-Standalone	SERB-SUPRA	29,50,000	Under Communication

	Dr. T.Devaraju	Photovoltaic system using Compact Multilevel Inverter			
6	Dr E Parimalasundar & Dr D Suresh Babu	Real time monitoring & fault identification in multilevel inverter fed Induction Motor drive	SVET	1,45,000	Under Progress
7	Dr. V. Arun	Design, Analysis and prototype development of Compact Multilevel Inverter with Reduced Switches for Medium Applications including PV system	SVET	50,000	Under Progress