SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati – 517 102

Department of EEE

SVEC/EEE/2018-2019

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REPORT on

Four day workshop on "Article Preparation Using Latex" under IEEE Student Chapter conducted from 08th to 11th August, 2018

The Department of Electrical and Electronics Engineering has organized a Four day workshop on "Article Preparation Using Latex" under IEEE Student Chapter from 08th to 11th August, 2018. The following are the topics covered in the programme.

Day 1 (08.08.2018):

Mr. Arun S L started the session with brief introduction about Latex software. The flexibility of Latex over Microsoft office while writing article, thesis and research manuscript had been extensively addressed by the resource person. Further, the installation procedure of Latex software and troubleshooting were also discussed.

Day 2 (09.08.2018):

The resource person explained the steps involved in formation of article chapter, section, subsection, subsubsection and paragraphs. Further, he discussed about the text formatting (bold, italic, underline, font size). In addition to these, the table preparation and alignment were also included in the session.

Day 3 (10.08.2018):

The resource person elaborated the steps for adding the figure in various places (top, middle, and bottom) of an article. Further, captioning of figure and table had also been discussed by him. The hyper-link of table and figure for further referencing were also explained during the session

Day 4 (10.08.2018):

Formatting of reference in various standard styles such as IEEE, Elsiver and Springer had been practiced by the participants. The Latex materials for easy reference and supporting software were shared with participants.

	Program Coordinator