

SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, A.Rangampet – 517 102

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

The Department of Electrical and Electronics Engineering, SVEC, organized a National level workshop on **“Emerging technologies in Electrical Engineering”** under Technical Education Quality Improvement Program-II, to provide the opportunity for the faculty, researcher scholars and PG students to know the Emerging Technologies in Electrical Engineering. The workshop will focus research challenges and latest developments in the emerging field of power electronic applications in power systems like multilevel inverters, custom power devices and FACTS controllers. About 85 members from various engineering colleges, Faculty and PG students of Department of EEE have participated in the programme.

Prof. T,Gopla Rao garu, Special officer inaugurated the program and gave his valuable suggestions. **Dr. D.V.S.S. Siva Sharma**, Professor, Dept. of EEE NIT, Warangal, is the Chief guest, enlightened the recent advances in the field of Electrical Engineering.

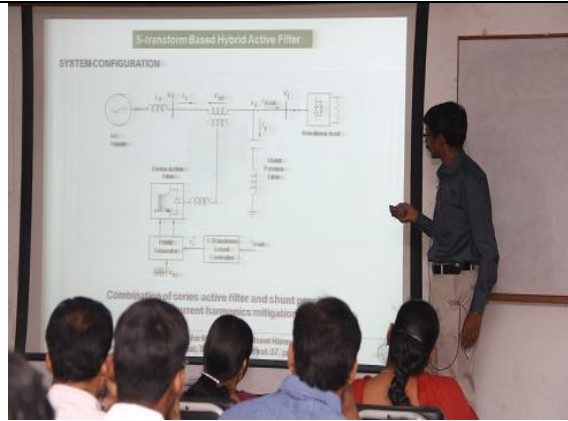
Dr. P.C Krishnamachary, Principal and **Prof P. Ramana Reddy** (Dean, PG courses) and **Dr.T.Devaraju**, Convener of the workshop welcomed the participants.

Dr.DVSS Siva Sharma, Dr.K.Shiva Kumar, Dr.C.Venkatesh and **Dr.T.Devaraju** are the resource persons for the workshop.

Resource person	Topic	Time
Day-1 Saturday (22-12-2012)		
Dr. D.V.S.S. Siva Sharma Professor, Department of EEE NIT Warangal	Power Quality Issues and mitigation	11:00 a.m to 11:45a.m
	SESSION – II	
	Wavelet applications in PQ Analysis and Transformers	12:00 Noon to 1:30p.m
Dr. K. Shiva Kumar, Asst Professor, Dept of EEE IIT, Hyderabad	Multilevel Inverters and its applications	2:30 p.m-5:00p.m
Day-2 Sunday (23-12-2012)		
SESSION – I		
Dr. C. Venkatesh Professor & Head, SREC ,Warangal	Harmonic reduction techniques	10:00 a.m to 12:00 Noon
SESSION – II		
Dr. T. Devaraju Head , EEE SVEC	Power system Stability using FACTS Controllers.	12:15 Noon to 1:00 p.m
SESSION – III		
Dr. C. Venkatesh Professor & Head, SREC , Warangal	PSCAD and Its applications in Electrical Engineering	2:00 p. m to 3:30 p. m

Mr. P.Ramesh, Asst Professor presented the vote of thanks.







Compiled By
Dr.T.Devaraju,HOD,EEE