SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, A. Rangampet – 517 102

Department of Electronics and Communication Engineering

Report on DST – Sponsored Workshop

"Research Issues in Advanced Signal Processing"

(20-21 August, 2015)

Workshop was organized by Department of Electronics and Communication Engineering, Sree Vidyanikethan Engineering College, A. Rangampet. The workshop was organized for two days i.e., August 20-21, 2015 under DST - SERB. About 40 faculty members have actively participated and gained useful information from all the sessions.

Day 1: 20-08-2015 (Thursday)

The workshop started at 9.00 AM with a welcome address by Dr.C. Subhas, Dean Academics and Advisor of the Program. Prof. P V Ramana, Professor & Head of ECE, and Convener of the Program briefed about the objective of the Workshop.

Session 1:

Dr. T. Kishore Kumar, Professor and Head, Department of ECE, NIT, Warangal discussed about various Signal Processing techniques and its Applications from 10.00 AM – 11.30 AM.

Session 2:

Dr. T. Kishore Kumar, Professor and Head, Department of ECE, NIT, Warangal delivered talk on Research issues in Advanced Signal Processing from 11.45 AM – 01.15 PM. The participants interacted with the resource person and he clarified their doubts regarding challenges and issues of Signal Processing.

Session 3:

Dr. T. Narayana Rao, Scientist- SF, NARL, Gadanki delivered talk on Wind Profiling of Radars from 2:15 PM – 3.30 PM.

Session 4:

Dr. T. Narayana Rao, Scientist- SF, NARL, Gadanki delivered talk on Research issues and methods of Wind Profiling of Radars from 3.45 PM – 5.00 PM. The participants interacted with resource person.

Day 2: 21-08-2015 (Friday)

Session 1:

Dr. G. Ramachandra Reddy , Senior Professor & Dean, Dept. of ECE, VIT, Vellore explained about Least Squares Filter Design in signal processing from 9:30 AM – 11.30 AM.

Session 2:

Dr. G. Ramachandra Reddy , Senior Professor & Dean, Dept. of ECE, VIT, Vellore explained about Eigen Filter Design Concepts used in signal processing and its applications from 11:45 AM – 1.15 PM.

Session 3:

Mr. J. Prem Kumar, Senior Application Engineer and **Mr. C. Naresh Babu**, Application Engineer, Capricot Tech. Pvt. Ltd, Bangalore, demonstrated the latest Tools used for signal processing using MATLAB from 2:15 PM – 3.30 PM. There was a training session in which many theoretical and practical illustrations using examples were discussed and demonstrated.

Session 4:

Mr. J. Prem Kumar, Senior Application Engineer and **Mr. C. Naresh Babu**, Application Engineer, Capricot Tech. Pvt. Ltd, Bangalore, demonstrated how to analysis of various Real-Time signals using MATLAB from 2.15 PM – 4.45 PM. The participants interacted with him to discuss about advanced methods and Tools used in Signal Processing.

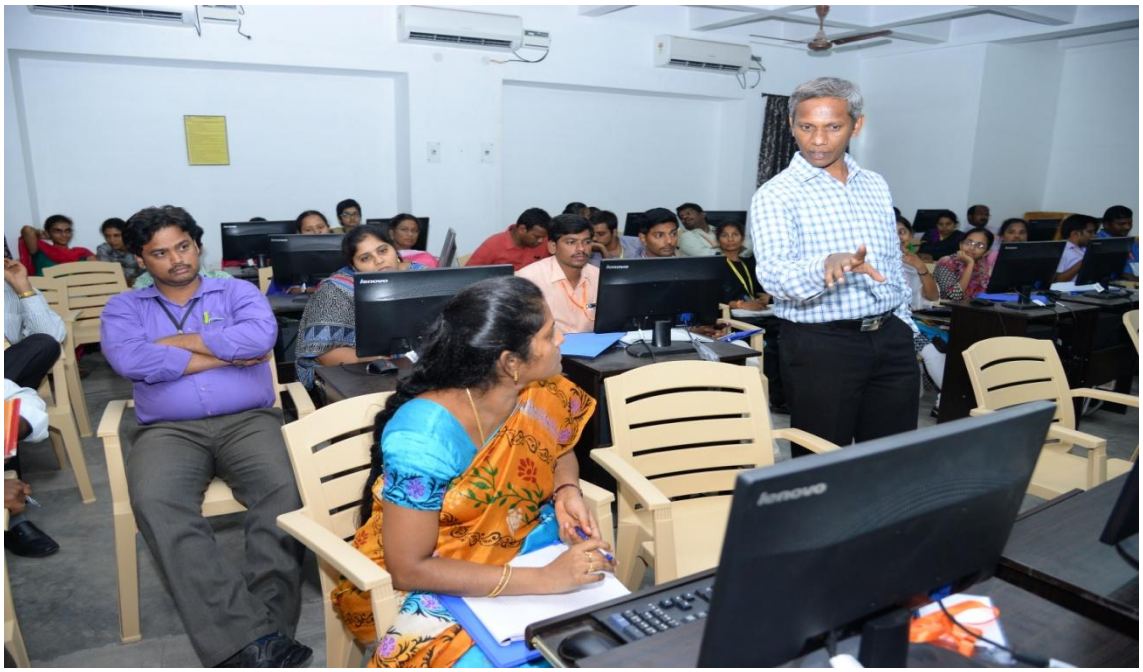
Participants registering for the Workshop

Dr. T. Kishore Kumar, Professor, NIT, Warangal delivering the lecture " Research Issues in Advanced Signal Processing Techniques".

Prof P V Ramana, Head of ECE and Convener of the Work shop, **Dr. C Subhash** , Dean Acadamics and **Dr. G Ramachandra** during the workshop.

Dr. G Ramachandra Reddy, Professor, VIT, Vellore delivering lecture on Least Squares and Eigen Filter Design

Participants during the Workshop



Mr. J. Prem Kumar, Senior Application Engineer, Capricot Tech. Pvt. Ltd, Bangalore demonstrating in Hands on training session