



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati – 517 102

TIME TABLE

2016 – 2017

ELECTRICAL & ELECTRONICS ENGINEERING

M. Tech. (Electrical Power Systems), II SEMESTER

Room No: 326

With effect from: 20-02-2017

TIME	9.40 to 10.30	10:30 to 11:20	11:20 to 12:10	12:10 to 1:00	B R E A K	2:15 to 3:05	3:05 to 3:55	3:55 to 4:45
DAY	←-----PSS-II LAB -----→ Room No: 110					FACTS	PQ	IS
MONDAY			RPS	SDPPS		PSSC	RPS	SDPPS
TUESDAY	←-----PSR LAB -----→ Room No: 109 F					FACTS	IS	PSSC
WEDNESDAY			PQ	FACTS		PSSC	RPS	SDPPS
THURSDAY			IS	FACTS		PQ	SDPPS	IPR
FRIDAY			RPS	IPR		IS	PSSC	PQ
SATURDAY								

SUBJECT NOTATION	SUBJECT NAME	SUBJECT CODE	NAME OF THE FACULTY
FACTS :	Flexible AC Transmission System	16MT20701	Dr. P. Umapathi Reddy
IS :	Intelligent Systems	16MT20702	Dr. M.S. Sujatha
PSSC :	Power System Stability and Control	16MT20703	Dr. N.M.G. Kumar
RPS :	Restructured Power System	16MT20704	Dr. S. Farook
SDPPS :	Static and Digital Protection of Power System	16MT20705	Dr. R. Selvarasu
PQ :	Power Quality	16MT20708	Ms. W. V. Jahnvi
PSR Lab :	Power Systems and Relays Lab Room No: 109 F	16MT20731	Mr. C. Kumar Reddy/ Dr. N.M.G.Kumar
PSS-II Lab :	Power Systems and Simulation – II Lab Room No: 110	16MT20732	Dr. S. Farook/ Mr. D. Suresh Babu
IPR :	Intellectual Property Rights (Audit Course)	16MT23810	Mr. I Kumara Swamy

HOD

Principal

Date:16-02-2017

Copy to:

1. The Principal's Office
2. HOD, EEE
3. The Examination Cell
4. NIVA Coordinator
5. DEAN (Academics)