SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, A.Rangampet – 517102

Department of Electronics and Instrumentation Engineering

SVEC/EIE/Seminar/2016   03.03.2016

Report

The Department of EIE organized one day seminar on **"Patenting and its challenges in E&IT"** on 8th September 2016.

The resource person was Mr. Rajasekar, Technical Head, AUZA Tech, Hyderabad. The following topics were covered in his lecture:

- Role of E&IT in india
- Opportunities and challenges in the domain of E&IT
- Patents received and Filling of patents in E&IT

Total 55 faculty members for SVEC were attended the program. Faculty members expressed their happiness about arrangement of this program.

Convener