



**SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)**

Sree Sainath Nagar, Tirupati-517 102

**Academic Calendar for the Year 2016-17**

**Master of Computer Applications  
(2014-15 Admitted Batch)**

<b>MCA V Semester</b>		
<b>I Spell of Instructions</b>	01-08-2016 to 17-09-2016	7 Weeks
<b>I Mid-term Examinations</b>	19-09-2016 to 23-09-2016	1 Week
<b>II Spell of Instructions</b>	24-09-2016 to 02-12-2016	9 Weeks
<b>II Mid-term Examinations</b>	05-12-2016 to 09-12-2016	1 Week
<b>Preparation and Practical</b>	10-12-2016 to 17-12-2016	1 Week
<b>End Examinations</b>	19-12-2016 to 31-12-2016	2 Weeks
<b>Commencement of Project Work for VI Semester</b>	02-01-2017	

<b>MCA VI Semester</b>		
<b>Commencement of Project Work</b>	02-01-2017	3 Weeks
<b>Submission of Abstract to the DC for Approval</b>	21-01-2017	
<b>Phase-I: Inception and Elaboration</b>	23-01-2017 to 25-03-2017	9 Weeks
<b>Oral Presentation of Progress with status Report</b>	20-03-2017 to 25-03-2017	
<b>Phase-II: Construction and Transition</b>	27-03-2017 to 27-05-2017	9 Weeks
<b>Oral Presentation of Progress with Status Report (Pre-submission seminar)</b>	22-05-2017 to 27-05-2017	
<b>Submission of Final Thesis / Dissertation</b>	After 27-05-2017 with successful completion of Pre-submission Seminar	

Dt : 26-07-2016

**DEAN (Academics)**

**PRINCIPAL**