SREE VIDYANIKETHAN ENGINEERING COLLEGE



(Autonomous)

Sree Sainath Nagar, Tirupati 517102.

Department of Electronics and Instrumentation Engineering

REPORT

The Department of EIE and ECONE jointly organized a National Level Faculty Development Programme on **Embedded Systems** during **10-11 October 2014**.

The main objective is to become well versed in the art of designing and developing Embedded Systems. Core concepts and various design options for Embedded Systems, Embedded C programming were discussed. An introduction on ARM processors, ARM programming Embedded basics was given.

Hands on training sessions were conducted on writing portable Embedded code using Emulators and Real hardware.

The following experts were the resource persons:

- 1. Mr. Mahesh Uttam Patil, Principal Technical Officer, CDAC, Hyderabad.
- **2. Dr. T.Nageswara Prasad,** Head, Department of EEE, SVEC, A.RAngampet.

A total of 43 faculty members were benefited by attending this programme.

The Outcomes of the program:

- 1. Acquired knowledge on ARM processors Architecture and Organization, Core concepts and design options of Embedded Systems.
- 2. Able to write portable Embedded code.

Convener



Greeting the resource person Mr. Mahesh Uttam Patil with a bouquet.



Greeting the resource person Dr. T.Nageswara Prasad with a bouquet.



Mr. Mahesh Uttam Patil delivering a lecture



Cross section of participants



Mr. Mahesh Uttam Patil delivering a lecture



Distribution of participation certificates by Mr. Mahesh Uttam Patil