**SREE VIDYANIKETHAN ENGINEERIGN COLLEGE**
**(Autonomous)**
Sree Sainath Nagar, Tirupati-517 102

**DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING**

SVEC/EIE/expert Lecture/2013 22.11.2013

## REPORT

The Department of EIE organized expert lecture on **"Making of System on Chip"** on 19th November 2013.

The resource person was **Mr. V. Muniswara Reddy**, Design Engineer, Krivi Semiconductor Ltd., Bengaluru.

The following topics were covered:

- Basics of embedded systems
- Importance of VLSI
- Design concepts of System on Chip (Front end and Back end steps)
- Basics of digital and analog hardware using Verilog
- Steps involved in converting an idea into a product

Total 345 students are attended the program. They expressed very happy about arranging this program.

**Convener**