

**SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)**

Sree Sainath Nagar, Tirupati - 517 102

Department of Electronics and Communication Engineering

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**SVEC-16**

**M. Tech. (VLSI)**

**Program Educational Objectives:**

After few years of graduation, the graduates of M. Tech. (VLSI) Program would have

- PEO1.** Enrolled or completed research studies in the core or allied areas of VLSI.
- PEO2.** Successful entrepreneurial or technical career in the core or allied areas of VLSI.
- PEO3.** Continued to learn and to adapt to the world of constantly evolving technologies in the core or allied areas of VLSI.