

SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)
Sree Sainath Nagar, Tirupati- 517 102

Department of Electronics and Communication Engineering

SVEC/ECE/M. Tech/05/2016-'17

20-08-2016

TIME – TABLE
ACADEMIC YEAR - 2016-2017
I M. Tech. - I Semester
VLSI

Room No. 716

w.e.f: 22-08-2016

DAY / Hour	11:20 - 12:10	12:10 - 01:00	L U N C H B R E A K	02:15 - 03:05	03:05 - 03:55	03:55 - 04:45
	1	2		3	4	5
MON	ICF	DM		DICD	CMM	RTS
TUE	RTS	RM		DM	ICF	DICD
WED	DICD LAB 9:40 AM to 1:00 PM			DICD	DM	AICD
THU	RM	RTS		DM	AICD	CMM
FRI	AICD LAB 9:40 AM to 1:00 PM			AICD	CMM	ICF
SAT	ICF	AICD		RTS	DICD	CMM

16MT15701	AICD	Analog IC Design	Ms. B. Gowthami
16MT15702	CMM	Computational Methods in Microelectronics	Ms. K. Neelima
16MT15703	DM	Device Modeling	Ms. M. Bharathi
16MT15704	DICD	Digital IC Design	Mr. K. Ramesh
16MT15705	ICF	IC Fabrication	Mr. G. Naresh
16MT15731	AICD LAB	Analog IC Design Lab	Ms. B. Gowthami
16MT15732	DICD LAB	Digital IC Design Lab	Ms. K. Neelima
16MT13808	RM	Research Methodology (Audit Course)	Dr. C. Subhas
16MT23808	RTS	Real Time Systems	Ms. Shaik Mehataz Begum

HOD, ECE

PRINCIPAL