



SREE VIDYANIKETHAN ENGINEERING COLLEGE

(Autonomous)

Sree Sainath Nagar, Tirupati - 517 102

TIME TABLE

2016 - 2017

ELECTRICAL & ELECTRONICS ENGINEERING IV - B.TECH. SECTION - A (II SEMESTER)

Room No: 323

With effect from: 09-01-2017

TIME	8:50-9:40	9:40-10:30	B R E A K	11:00-11:50	11:50-12:40	12:40-1:30
DAY	1	2		3	4	5
MONDAY	ERTS	DEP		DBMS	ERTS	DBMS
TUESDAY	DEP	ERTS		DBMS	DEP	ERTS
WEDNESDAY	DBMS	ERTS		ERTS	DBMS	DEP
THURSDAY	DEP	DBMS		DEP	ERTS	DBMS
FRIDAY	DEP	ERTS		DBMS	DEP	ERTS
SATURDAY	DBMS	DEP	ERTS	DBMS	DEP	

SUBJECT NOTATION		SUBJECT NAME	SUBJECT CODE		NAME OF THE FACULTY
ERTS	:	Embedded and Real Time Systems	10BT70405	:	Mr. G. Hemanth Kumar
DBMS	:	Database Management Systems	10BT50503	:	Mr. B. D. Kalyan Kumar
DEP	:	Distribution of Electrical Power	10BT80202	:	Mr. D. Sreenivasulu Reddy

Date: 05-01-2017

Head

Principal

Copy to:

1. The Principal's Office
2. The Examination Cell
3. DEAN (Academics)
4. HOD, EEE
5. NIVA Coordinator