



SREE VIDYANIKETHAN ENGINEERING COLLEGE
(Autonomous)
Sree Sainath Nagar, Tirupati – 517 102

Department of Computer Science and Systems Engineering
TIME TABLE
(2016-2017)
IV B.Tech II Semester (Section-A)

Room No: 516

w.e.f.:14-12-2016

DAY /HR	8:50- 9:40	9:40-10:30		11:00-11:50	11:50-12:40
MON	MC	STT	B R E A K	MS	MC
TUE	STT	MC		STT	MS
WED	MS	MC		STT	STT
THU	MS	MC		MS	STT
FRI	STT	MC		MS	MC
SAT	MC	MS		STT	MS

	SUBJECT CODE	SUBJECT NAME	FACULTY NAME
MS	10BT6HS01	MANAGEMENT SCIENCE	Mr. G. Vidyasagar Reddy
STT	10BT70502	SOFTWARE TESTING TECHNIQUES	Dr. B. Narendra Kumar Rao
MC	10BT71202	MOBILE COMPUTING	Mr. B. Narendra

HOD, CSSE

PRINCIPAL



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Sree Sainath Nagar, Tirupati – 517 102

Department of Computer Science and Systems Engineering
REVISED-TIME TABLE
(2016-2017)

IV B.Tech II Semester (Section-B)

Room No: 715

w.e.f.:23-01-2017

DAY /HR	8:50- 9:40	9:40-10:30	B R E A K	11:00-11:50	11:50-12:40
MON	MC	STT		MS	STT
TUE	MS	MC		MC	STT
WED	MS	STT		MC	MS
THU	MC	STT		MS	MC
FRI	STT	MS		STT	MC
SAT	STT	MS		MC	MS

	SUBJECT CODE	SUBJECT NAME	FACULTY NAME
MS	10BT6HS01	MANAGEMENT SCIENCE	Mr. K. Harsha Vardhan Reddy
STT	10BT70502	SOFTWARE TESTING TECHNIQUES	Ms. O. Rajitha
MC	10BT71202	MOBILE COMPUTING	Ms. C. Sushama

HOD, CSSE

PRINCIPAL