



**SREE VIDYANIKETHAN ENGINEERING COLLEGE  
(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet-517 102

**DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING**

*Cordially invites to Seminar on*

**“Benefits of Patents to MSMES And Startups”**

By

**Dr M Naresh Babu**

**Associate Professor, Dept of CSSE**

**Date: January 24, 2017**

**Time: 2:30 PM to 4:30 PM**

**Venue: E-Class Room**

**Target Group: IV B.Tech CSSE Students, Faculty of CSSE**