



**SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet-517 102

DEPARTMENT OF COMPUTER SCIENCE AND SYSTEMS ENGINEERING

Cordially invites to Seminar on

**“INTELLECTUAL PROPERTY RIGHTS & STEPS IN PATENT
GRANTING”**

By

Mr B Narendra Kumar Rao

Associate Professor, Dept of CSE

Date: January 27, 2015

Time: 2:30 PM to 4:30 PM

Venue: E-Class Room

Target Group: IV B.Tech CSSE Students & Faculty of CSSE