



**SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)**

Sree Sainath Nagar, A. Rangampet – 517 102

Department of Computer Science and Systems Engineering

**FACULTY DEVELOPMENT PROGRAMME on
High Performance Computing**

The Department of CSSE organized FDP on "High Performance Computing", 30-31, August 2013 under TEQIP – II.

This Faculty Development Programme covered both hardware and software aspects of High Performance parallel and distributed systems. Mainly focused on Fundamentals of HPC- Parallel Computing, Services of HPC, Parallel Programming with MPI, Introduction to Multicore Architectures, Parallel programming with OpenMP, Parallel Algorithms for Multicore Architectures, Green HPC, HPC & Cloud.

The faculty from various colleges across the nation attended the FDP.



Inaugural Function of Faculty Development Programme on "High Performance Computing"



Dr. V.C.V. Rao, C-DAC, Pune delivering lecture on "Fundamentals of HPC Parallel Computing, Processor Architecture for HPC, Parallel Programming with MPI"



Dr. Amit Parashuram Kesarkar, NARL, Gadanki discussing "Introduction to Multicore Architectures"



Dr. Chapram Sudhakar, NIT, Warangal delivering lecture on "Green HPC, HPC & Cloud"



Dr. M. Rajasekhar Babu, VIT, Vellore delivering lecture on "Parallel Programming with OpenMP, Parallel Algorithms for Multicore Architectures"



Group Photo