



**SREE VIDYANIKETHAN ENGINEERING COLLEGE
(AUTONOMOUS)**

Sree Sainath Nagar, Tirupati-517 102

Academic Calendar for the Year 2016-17

**Master of Computer Applications
(2014-15 Admitted Batch)**

MCA V Semester		
I Spell of Instructions	01-08-2016 to 17-09-2016	7 Weeks
I Mid-term Examinations	19-09-2016 to 23-09-2016	1 Week
II Spell of Instructions	24-09-2016 to 02-12-2016	9 Weeks
II Mid-term Examinations	05-12-2016 to 09-12-2016	1 Week
Preparation and Practical	10-12-2016 to 17-12-2016	1 Week
End Examinations	19-12-2016 to 31-12-2016	2 Weeks
Commencement of Project Work for VI Semester	02-01-2017	

MCA VI Semester		
Commencement of Project Work	02-01-2017	3 Weeks
Submission of Abstract to the DC for Approval	21-01-2017	
Phase-I: Inception and Elaboration	23-01-2017 to 25-03-2017	9 Weeks
Oral Presentation of Progress with status Report	20-03-2017 to 25-03-2017	
Phase-II: Construction and Transition	27-03-2017 to 27-05-2017	9 Weeks
Oral Presentation of Progress with Status Report (Pre-submission seminar)	22-05-2017 to 27-05-2017	
Submission of Final Thesis / Dissertation	After 27-05-2017 with successful completion of Pre-submission Seminar	

Dt : 26-07-2016

DEAN (Academics)

PRINCIPAL